Communication Topology Analysis for Concurrent Programs

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Abstract. In this article, we address the problem of statically determining an approximation of the communication topology of concurrent programs. These programs may contain dynamic process and channel creations and may communicate channel names as well as functions, possibly containing other communications.
We introduce a control flow analysis which builds finite state automata to improve its precision. The method is twofold. First, we build an automaton for each process in the concurrent system yielding an approximation of how the synchronizations realized by the sequential components are ordered. Second, we extract the communication topology from a reduced product automaton, which size is polynomial in the size of the original program. This analysis was implemented and we apply it to the verification of a circuit allocation mechanism.

1 Introduction

Static analysis is a widely used technique to establish properties satisfied by programs, independently of a given execution context. Most common applications include compile-time optimizations and program verification. However, this scope is extended by concurrency which introduces new problems as well as new applications. Recently, much research has been done in this area, including Amtoff et al. [2], Bodei et al. [4, 5], Jagannathan [16], Kobayashi et al. [11], Marinescu and Goldberg [12], Nielson and Nielson [15–17, 19].

In this article, we present a static analysis which computes a fine approximation of the communication topology of Concurrent ML programs [23] and, more generally, which analyzes the whole synchronizations of concurrent programs. By determining, for each reception, the set of possibly matching emissions, we find an approximation of the possibly received values.

Our analysis is a control flow analysis (CFA). Concerning sequential functional languages, the aim of a CFA is to detect which functions an expression can evaluate to [18, 20, 24, 25]. For higher order concurrent functional languages, such as Concurrent ML [23], this task is more complicated. Channel names may
be dynamically created and communicated anywhere in the program, new processes can be dynamically created and functions are first-class values which may be communicated. Hence, a piece of code in the source program (possibly containing communications) may be executed by any process. Such analyses have been proposed by Bodei et al. [4], Colby [6], Solberg et al. [26] and Mercouloff [14]. However, the precision of the CFA is closely related to the approximations made on the topology of the communications. Hence, we address the problem of minimizing for each emission, the set of possible receptors. Eliminating some impossible communications improves the annotations at reception points and, consequently, on the sequential parts of the program using the received values.

From a technical point of view, we proceed as follows. First, we order the synchronization primitives of the sequential processes in the system. This is done by building a finite automaton \( \tilde{A}_p \) for each process \( p \). A labeled path denotes one possible sequence of synchronizations in \( p \). Second, we approximate how the different processes may interact altogether by building the product of the \( \tilde{A}_p \)'s. This product automaton possibly has size exponential in the size of the program. Hence, we introduce a reduced product automaton whose size is polynomial and which conservatively approximates the product automaton.

Applications of CFA for concurrent languages are twofold. First, mixing a CFA with another static analysis usually improves the precision of this latter. For instance, partial evaluation [7] of concurrent languages has been discussed by Marinescu and Goldberg [12] and by Gengler and Martel [8, 9]. Marinescu and Goldberg [12] also introduce a binding time analysis (BTA) which collects informations used by the partial evaluator. However, this BTA makes rough approximations due to the lack of informations about the topology of communications. Using a CFA would improve the performances of the partial evaluator. Second, as outlined by Bodei et al. [4], another application of CFA for concurrent languages are security and program verification. For a concurrent program, a CFA allows one to statically determine whether two given processes \( P_1 \) and \( P_2 \) may communicate together at one stage of their execution, or to statically approximate the set of channels which are shared by \( P_1 \) and \( P_2 \) during their execution. For instance, these results enable one to check access rights properties.

Section 2 gives an informal overview of the techniques developed in this article. Section 3 briefly introduces the subset of Concurrent ML we use. We introduce the analysis in Sections 4 and 5. First, in Section 4 we define an analysis for the individual processes and second, in Section 5, we introduce two ways to analyze a pool of processes, respectively based on product and reduced product automata. This analysis was implemented and results are discussed in Section 6 for a virtual circuit allocation mechanism, similar to the one used in ATM.

2 General Description

To compute precise annotations, a CFA has to use a fine approximation of the topology of the communications realized by a program. In this Section, we illustrate how our analysis works using the concurrent program of Figure 1 a).
In this system, Processes $p_1$ and $p_3$ respectively realize two and one emissions, and $p_2$ realizes three receptions. We assume that all the communications are made on the same channel. Communications are synchronous, i.e., an emission or a reception blocks the process until the communication occurs. In the remainder of this article, we use the following vocabulary. A communication is the synchronous interaction between an emission point and a reception point. A synchronization is either a communication or a new process creation (a fork). A synchronization point is either a communication point or a fork point.

A first way to conservatively annotate $p_2$ is to consider that any reception may receive its value from any emission on the same channel [26]. Let us call $s_1$ and $s_2$ the emissions of $p_1$, $s_3$ the emission of $p_3$, and $r_1$, $r_2$, and $r_3$ the receptions of $p_2$. Let $\hat{C}(s_i)$ (resp. $\hat{C}(r_j)$) be the abstract value sent (resp. received) at $s_i$ (resp. $r_j$), $1 \leq i,j \leq 3$. Such an analysis assigns to $\hat{C}(r_1)$, $\hat{C}(r_2)$ and $\hat{C}(r_3)$ the values

$$\hat{C}(r_j) = \bigcup_{i \in \{1,2,3\}} \hat{C}(s_i) \quad j \in \{1,2,3\} \quad (1)$$

As sketched in Figure 1 b), Equation (1) describes a correct approximation of the communication scheme of Figure 1 a). However, it is a trivial matter of fact that $s_1$ cannot communicate with $r_3$ nor $s_2$ with $r_1$.

![Fig. 1](image.png)

**Fig. 1.** a) A system made of communicating processes. b) Approximation of the communication topology computed by classical methods. c) Approximation of the topology computed by our CFA.

In order to assign a fine abstract value to receptions, a CFA has to approximate closely the communication topology of programs. This problem is twofold and we address it as follows.

- Obviously, the communication graph of a concurrent program depends on how the communications are ordered on each process. In this article, this point is addressed as follows. We build a finite state automaton $\tilde{A}$ which indicates how the synchronization points possibly follow each other. For each labeled sub-expression $\tilde{e}'$ in the program, we use an automaton whose initial and final states are identified by the nodes $\bar{v}_i$ and $\bar{v}_f$. The transitions
between these nodes depends on the expression. The transitions between sub-
expressions indicate their respective evaluation order. A transition related
to a sequential reduction step is labeled \( \varepsilon \) and we introduce a \( l \)-transition to
denote the occurrence of a synchronization point labeled \( l \). The result is an
approximation of the communication scheme of each process.

Knowing how the synchronizations are ordered on each process, we have to
determine their interactions. A first way to address this problem is to con-
sider the product of the automata related to the different processes in the
program. Based on the collection \( \hat{S} = (\hat{A}_p)_{p \in \text{Dom}(P)} \) of automata describ-
ing the synchronization points of the different processes, we show that the
product automaton \( A^P_{\hat{S}}(\hat{S}) \) describes a correct approximation of the synchr-
onizations the program may realize. However, on one hand the size of \( A^P_{\hat{S}}(\hat{S}) \)
is possibly exponential in the size of the program, while on the other hand it
contains more informations than really needed for the analysis. Intuitively,
the only relevant informations in \( A^P_{\hat{S}}(\hat{S}) \) for the analysis are the points which
may synchronize together. Hence, we introduce a reduced product automa-
ton \( A_{\hat{S}}(\hat{S}) \) which is polynomial in the size of the source program and we prove
that \( A_{\hat{S}}(\hat{S}) \) correctly approximates \( A^P_{\hat{S}}(\hat{S}) \). The result is an approximation
of the possible interactions between processes.

In Figure 1 c), we show the approximation of the topology obtained using
the method described above for the communication scheme of Figure 1 a). In
the remainder of this Section we discuss the approximations made by our CFA.

First, the abstract value attached to a sub-expression is the union of all the
abstract values carried by the different reduction paths of a non-deterministic
program. For communications, this means that the value attached to a reception
is the union of all the values carried by the potential emitters. This corresponds
to the approximation done in Figure 1 c). Such approximations make the control
flow annotations grow. However, any abstract value collected this way is related
to one of the concrete values in one of the possible communication schemes.

Other approximations are obviously done during the analysis of the body of
loops. First, different channel names created by the same channel instruction
inside a loop are identified. Hence, communications over such channels are
assumed to be possible, even though the emitter and the receptor use different
instances of the same instruction.

Second, a process \( p \) may communicate with the communication points oc-
curring inside the body \( b \) of a loop, as long as \( p \) assumes that the loop has not terminated. However, thanks to the sequential automata, the analysis keeps
track of the communication ordering inside \( b \). The only sequences allowed are
those corresponding to an unfolding of \( b \).

For instance, let us consider the program of Figure 2 which describes a
multiplexer-demultiplexer of channels. This program is made of two processes \( p_1 \)
and \( p_2 \) written in Concurrent ML. Each sub-expression is labeled by an integer,
\( p_1 \) alternatively receives data on channels \( t_1 \) and \( t_2 \). These data are transmitted
on a channel \( \gamma \), \( p_2 \) receives on \( \gamma \) the multiplexed data and transmits them on \( o_1 \)
and \( o_2 \). We assume that \( \gamma \) is only shared by \( p_1 \) and \( p_2 \). So, the data sent on \( o_1 \)
(resp. \( o_2 \)) are the ones the multiplexer received on \( t_1 \) (resp. \( t_2 \)). Finally, a process \( p_3 \) sends new channel names to the multiplexer on \( t_1 \) and \( t_2 \).

\[
\begin{align*}
p_1 : \text{rec}^3 \text{ multiplex } t_1 \ t_2 \ \gamma = \\
\text{let}^7 c_1 := \text{send}^8 \ \gamma (\text{receive}^4 \ t_1) \\
\text{and}^8 c_2 := \text{send}^9 \ \gamma (\text{receive}^5 \ t_2) \\
\text{in} (\text{multiplex } t_1 \ t_2 \ \gamma)^8
\end{align*}
\[
\begin{align*}
p_2 : \text{rec}^9 \text{ demultiplex } o_1 \ o_2 \ \gamma = \\
\text{let}^{10} c_1 := \text{send}^{11} \ o_1 (\text{receive}^{12} \ \gamma) \\
\text{and}^{13} c_2 := \text{send}^{14} \ o_2 (\text{receive}^{15} \ \gamma) \\
\text{in} (\text{demultiplex } o_1 \ o_2 \ \gamma)^{16}
\end{align*}
\]

\[
p_3 : \text{rec}^{17} \text{ emitter } t_1 \ t_2 = \\
\text{let}^{18} e_1 := \text{send}^{19} \ t_1 \ \text{channel} \ O^{20} \\
\text{and}^{21} e_2 := \text{send}^{22} \ t_2 \ \text{channel} \ O^{23} \\
\text{in} (\text{emitter } t_1 \ t_2)^{24}
\]

Fig. 2. Multiplexer - demultiplexer written in Concurrent ML.

Our implementation of the analysis determines that the abstract value emitted at point 11 (resp. 14) is the singleton \{20\} (resp. \{23\}). As stated before, different channel names created at the same point are not distinguished. However, the CFA detects that the instance of the first reception on \( \gamma \) (point 12) only may receive a value sent by an instance of the first emission (point 3). Similar observations can be done for the second emission and reception on \( \gamma \).

3 Concurrent ML

In this Section, we introduce the subset of Concurrent ML [3, 21, 22] used in this article to illustrate how our analysis works. The syntax is defined in Figure 3.

Labels are attached to terms. They are used during the analysis which assumes that each sub-expression in the initial program has a unique label. This property is not maintained by reduction. The language contains conditionals, a let construct, and the operator rec for recursive functions. channel \( O \) denotes a function call which creates and returns a new channel name \( k \) different from all the existing ones. (fork \( e^0_k \)) creates a new process which computes \( e^0_k \) and evaluates to \( O \) (the value of type unit). (send \( e^0_k \ e^0_j \)) is the blocking emission of the value of \( e^0_k \) on the channel resulting from the evaluation of \( e^0_k \). \( e^0_k \) and \( e^0_j \) respectively are the subject and the object of the communication. Once the communication is done, a send evaluates to the value of its object. (receive \( e^0_k \)) is the blocking reception of a value on the channel name described by \( e^0_k \) (the subject of the reception). Values are in the domains of basic types or channel names or functions.
The operational semantics, given in Figure 4, are based on the language $\lambda_{\text{p}}$ defined by Reppy [21], $\rightarrow$ is used for sequential reduction steps. $e_0 \{x \leftarrow e_1\}$ denotes the term obtained by discarding the labels of the occurrences of $x$ in $e_0$ and substituting $e_1$ to $x$.

$\rightarrow$ is used for concurrent reduction steps. These steps are annotated with labels $\ell \in \text{Lab} \cup \{\varepsilon\}$ which only are used in order to prove the correctness of the analysis. An $\varepsilon$-labeled step corresponds to a sequential reduction step made by one of the processes in the pool. The transition related to a communication between two instructions labeled $l_a$ and $l_r$ is annotated $l_a,l_r$ and the reduction step related to a process creation is annotated $l_f,l_f$ where $l_f$ is the label of the fork. Following Reppy [21], we use evaluation contexts $E$ defined in Figure 3.

In addition, we use the classical notion of configuration $K,P$ to denote a concurrent system in Concurrent MLL. $K$ is the environment for channels and $P$, the process pool, is defined in Figure 3.

A process is defined by $\langle p; e \rangle$, $p$ being the name associated to the process which computes $e$. In Figure 3, $\varepsilon\rightarrow$ is defined over process pools. Inside a process, a sequential reduction step is done using $\leftarrow$.

For the instruction $(\text{channel}_O)^f$, a new channel name $k$ is assigned to the expression and the set $K$ of used channel names is enriched with $k$. When an expression is forked, the father evaluates to $O$ and the new process is named $q$, where $q$ is a fresh process name. Finally, a communication may occur between an emitting process $p_e$ and a receiving process $p_r$ which use the same channel. In this case, both $p_e$ and $p_r$ evaluate to the value exchanged.

4 Analysis of the Sequential Expressions

In this section, we introduce the basic analysis within a sequential process. Section 5 presents the analysis between processes. Note that there are not separable analyses, because the interactions between processes affect the control-flow analysis of the individual processes. If $e'$ contains fork's, the bodies of the child
processes are analyzed at the same time as \( e' \), but not their interactions whose analysis is deferred to Section 5.

The analysis of an expression \( e' \) is a triple \( (\hat{C}, \hat{E}, \hat{A}) \). Let \( \text{Lab} \) and \( \text{Id} \) respectively denote the sets of labels and variables occurring in \( e', \hat{C}, \hat{E} \) and \( \hat{A} \) are defined as follows.

- \( \hat{C} : \text{Lab} \to \wp(\text{Lab}) \) is the abstract cache which approximates, for any sub-expression \( e' \) in the program, the set \( \hat{C}(l) \) of values \( e' \) may evaluate to. The abstract values either are functions denoted by their labels or channel names denoted by the label of the instruction \( \text{channel} \) which created them. Type discipline ensures that no confusion is made between both kinds of values and avoids the introduction of two different caches.

- \( \hat{E} : \text{Id} \to \wp(\text{Lab}) \) is the abstract environment which binds free variables during the analysis.

- \( \hat{A} = (\Sigma, B, Q, Q_f, \delta) \) is a finite automaton which indicates how the synchronizations are ordered inside the expression we analyze. \( \Sigma = \text{Lab} \cup \{\varepsilon, \mu\} \) is the alphabet, \( Q \) is the set of states, \( B \in Q \) is the initial state, \( Q_f \subseteq Q \) contains a unique final state, \( \delta \subseteq (Q \times \Sigma) \rightarrow Q \) is the transition function.

In Figure 5, we define inductively on the structure of the terms the constraints a triple \( (\hat{C}, \hat{E}, \hat{A}) \) has to satisfy in order to define a correct analysis for an expression \( e' \). If so, we write \( \hat{C}, \hat{E}, \hat{A} \vdash e' \).

We use the following notations, \([s \xleftarrow{} s']\) denotes an automaton whose initial and final state respectively are \( s \) and \( s' \) and such that \( Q = \{s, s'\} \) and \( s' \in \delta(s, e) \).
Fig. 5. Specification of the analysis.

$\mathcal{A} \subseteq \mathcal{R}$ states that $\mathcal{A}$ is a sub-automaton of $\mathcal{R}$, i.e., $Q \subseteq Q'$ and $\delta \subseteq \delta'$. Hence, $[s \xrightarrow{\delta} s'] \subseteq \mathcal{A}$ indicates that there is a $\delta$-transition from $s$ to $s'$ in $\mathcal{A}$. The analysis builds for each expression $e'$ an automaton $\mathcal{A}$ which initial and final states respectively are $\text{Start}(\mathcal{A}) = B_I$ and $\text{End}(\mathcal{A}) = \{E_I\}$. These automata are defined inductively on the structure of the terms. Their graphical representation is given in Figure 6. $\varepsilon$-transitions denote sequential reduction steps.

Because the evaluation of a first order constant $c'$ does not involve any synchronization, the automaton associated to this expression is made of an $\varepsilon$-transition from the initial state $B_I$ to the final state $E_I$, as shown in Figure 6. This is ensured by the constraint $[B_I \xrightarrow{\varepsilon} E_I] \subseteq \mathcal{A}$ in the specification of Figure 5.

For a variable $x'$, $\mathcal{E}(x)$ is added to $\mathcal{C}(l)$ indicating that the abstract value of point $l$ depends on the abstract value of $x$ in the environment. In addition,
because no reduction may come from the evaluation of a variable, the automaton related to $x'$ is made of an $\varepsilon$-transition between the states $B_1$ and $E_l$. Again, this is ensured by the constraint $[B_1 \rightarrow E_l] \subseteq \hat{A}$.

$(\text{fun } x'^1 \Rightarrow e_0^{l_1})^l$ and $(\text{rec } f \ x'^1 \Rightarrow e_0^{l_1})^l$ are values. Their evaluation does not require any computation. Hence, just like for first order constants, the initial state $b_l$ of the automaton is linked to the final state $E_l$ by an $\varepsilon$-transition. In addition, the body $e_0^{l_1}$ is analyzed, yielding an automaton $\hat{A}_0$ with $B_{l_0}$ and $E_{l_0}$ as initial and final states. As depicted in Figure 6, we connect $\hat{A}_0$ to $\hat{A}$ by a $\mu$-transitions via the constraints $[B_1 \rightarrow B_{l_0}] \subseteq \hat{A}$ and $[E_{l_0} \rightarrow E_l] \subseteq \hat{A}$. $\mu$-transitions only are used to link both parts of the automaton and never describe a valid path between nodes. Concerning the abstract cache, $I$ is added to $C(I)$, indicating that the current function is among the ones which may occur at this point.

The analysis of an application $(e_0^{l_0}, e_1^{l_1})^l$ first relies on these of $e_0^{l_0}$ and $e_1^{l_1}$ which yield two automata $\hat{A}_0$ and $\hat{A}_1$. Because of the evaluation order, the synchronizations made by $e_0^{l_0}$ precede the ones made by $e_1^{l_1}$. Also, assuming that $e_0^{l_0}$ evaluates to $(\text{fun } x'^1 \Rightarrow e_0^{l_2})^l$, the synchronizations made by $e_1^{l_1}$ precede the ones resulting from the evaluation of $e_0^{l_2}$ with the right value for $x$. Hence we build the following automaton, $B_l$ is linked to the initial state of $\hat{A}_0$ and $\hat{A}_0$'s final state is linked to $\hat{A}_1$ initial state by $\varepsilon$-transitions. This corresponds to the constraints $\hat{A}_0 \subseteq \hat{A}, \hat{A}_1 \subseteq \hat{A}, [B_l \rightarrow B_{l_0}] \subseteq \hat{A}$ and $[E_{l_0} \rightarrow E_l] \subseteq \hat{A}$. 

Fig. 6. Automata attached to the expressions during the analysis.
Next, $\tilde{C}(l_0)$ denotes the labels of the functions $\mathcal{A}_0$ may evaluate to. For each function $(\text{fun} \ x^4 \Rightarrow \mathcal{A}_0^4)^2$ such that $l_0 \in \tilde{C}(l_0)$, we indicate that the synchronizations in the body $\mathcal{A}_0^4$ follow these in $\mathcal{A}_1^1$ by asking $\llbracket \mathbf{E}_0 \xleftarrow{r} \mathbf{E}_0 \rrbracket \subseteq \mathcal{A}_2$. Finally, the final states of $\mathcal{A}_1$ and $\mathcal{A}_2$ are linked by $\llbracket \mathbf{E}_0 \xleftarrow{r} \mathbf{E}_0 \rrbracket \subseteq \mathcal{A}_3$. $\text{prg}$ denotes the program for which we compute an analysis. In Figure 6 we show the automaton resulting from the analysis of an expression $(\mathcal{A}_0^4 \mathcal{A}_1^1)^l$, assuming that $\tilde{C}(l_0) = \{l_2\}$ and that $(\text{fun} \ x^4 \Rightarrow \mathcal{A}_0^4)^2 \in \text{prg}$.

The application of a recursive function is analyzed similarly. Because the body may be executed zero or many times, we add $\llbracket \mathbf{B}_l \xleftarrow{r} \mathbf{B}_l \rrbracket \subseteq \mathcal{A}_2$ and $\llbracket \mathbf{E}_l \xleftarrow{r} \mathbf{E}_l \rrbracket \subseteq \mathcal{A}_3$, where $l_4$ is the label of the body of the recursive function.

Channels are identified by their creation points. channel() is a function call, for an occurrence of channel() in the program, we collect $l$ in $\tilde{C}(l)$. So, no distinction is made between different channels created in a recursive function.

For an expression $(\text{fork} \ \mathcal{A}_0^4)^l$, we only analyze the body $\mathcal{A}_0^4$ of the child process at this stage of the analysis, obtaining an automaton $\mathcal{A}_0$. In Section 5, during the analysis of the interactions between processes, we consider fork's just like another communications. For now, $\llbracket \mathbf{B}_l \xleftarrow{r} \mathbf{B}_l \rrbracket \subseteq \mathcal{A}_2$ indicates that the execution of the fork consists of doing a synchronization denoted $l$. On the other hand, the automaton $\mathcal{A}_0$, which describes the synchronizations realized by $\mathcal{A}_0^4$ is included in $\tilde{A}$ and linked to the initial state $\mathbf{E}_0$ of the expression we analyze by $\llbracket \mathbf{B}_l \xleftarrow{r} \mathbf{E}_0 \rrbracket \subseteq \mathcal{A}_3$. In Section 5.2, when considering the product automaton, $\mathcal{A}_0^4$ stays frozen until it can synchronize on $l$ with the fork.

The specification of the analysis for an emission $(\text{send} \ \mathcal{A}_0^4 \mathcal{A}_1^1)^l$ states that the sub-expressions are analyzed. $\llbracket \mathbf{E}_0 \xleftarrow{r} \mathbf{E}_0 \rrbracket \subseteq \mathcal{A}_2$ indicates that the execution of $\mathcal{A}_0^4$ precedes the one of $\mathcal{A}_1^1$ and $\llbracket \mathbf{B}_1 \xleftarrow{r} \mathbf{B}_1 \rrbracket \subseteq \mathcal{A}_3$ states that $l$ denotes the last synchronization point in $(\text{send} \ \mathcal{A}_0^4 \mathcal{A}_1^1)^l$.

Receptions are treated similarly: $\llbracket \mathbf{E}_0 \xleftarrow{r} \mathbf{E}_0 \rrbracket \subseteq \mathcal{A}_2$ states that the synchronizations in the sub-expression precede the synchronization related to the reception. Note that no constraint is introduced on the received abstract value at this stage of the analysis. This is due to the fact that $\tilde{C}, \tilde{E}, \tilde{A}, \vdash \mathcal{A}$ only specifies the analysis of the different processes without considering their synchronizations. However, in a concurrent execution, a reception (receive $\mathcal{A}_0^4$) may receive either a basic value or a function or a name, depending on its type. Hence, in Sections 5.1 and 5.2, when considering process interactions, we introduce constraints indicating how $\tilde{C}(l)$ is bound to. These constraints are specified in definitions 4 and 8.

As an intermediary result, we introduce some properties satisfied by the analysis defined in Figure 5. First we examine the existence of a best analysis for an expression $\mathcal{A}^l$ and, second, we focus on correctness. These results are used further in this article, when proving the main property concerning the correctness of the specification over concurrent reduction.

Let $(\tilde{C}_1, \tilde{E}_1, \tilde{A}_1)$ and $(\tilde{C}_2, \tilde{E}_2, \tilde{A}_2)$ be two analyses for the same expression $\mathcal{A}^l$. $(\tilde{C}_1, \tilde{E}_1, \tilde{A}_1) \prec (\tilde{C}_2, \tilde{E}_2, \tilde{A}_2)$ denotes that $(\tilde{C}_1, \tilde{E}_1, \tilde{A}_1)$ is more precise than
\((\tilde{C}, \tilde{\mathcal{E}}, \tilde{A}), \prec \) is defined by

\[
(\tilde{C}, \tilde{\mathcal{E}}, \tilde{A}) \prec (\tilde{C}', \tilde{\mathcal{E}}, \tilde{A}') \iff \begin{cases} \\
\forall l \in \text{Lab}, \; \tilde{C}(l) \subseteq \tilde{C}'(l) \\
\forall x \in \text{Id}, \; \tilde{\mathcal{E}}_1(x) \subseteq \tilde{\mathcal{E}}_2(x) \\
\tilde{A}_1 \subseteq \tilde{A}_2 
\end{cases}
\]

(2)

The existence of a least analysis in the sense of \(\prec\) stems from the fact that, for an expression \(c'\), the set \(\{ (\tilde{C}, \tilde{\mathcal{E}}, \tilde{A}) : \tilde{C}, \tilde{\mathcal{E}}, \tilde{A} \vdash c' \}\) is a Moore family\(^1\).

The main property introduced in this Section concerns sequential subject reduction and indicates that if a triple \((\tilde{C}, \tilde{\mathcal{E}}, \tilde{A})\) is a correct analysis for an expression \(c'\) then it is still correct after a sequential reduction step \(\leftrightarrow\) (Section 3). First, we introduce the order relation \(\prec\) over automata.

**Definition 1** Let \(\tilde{A}\) and \(\tilde{A}'\) be two automata. \(\tilde{A} \prec \tilde{A}'\) iff any path labeled \(\ell_1, \ldots, \ell_n\) in \(\tilde{A}\) is a path in \(\tilde{A}'\).

Intuitively, the labels of the transitions in an automaton \(\tilde{A}\) built during the analysis of an expression \(c'\) correspond to the synchronizations realized during the execution of \(c'\). Hence, a path in \(\tilde{A}\) denotes one possible sequence of synchronizations the execution of \(c'\) may lead to. Sequential reduction steps do not realize synchronizations but may discard some possible sequences of synchronizations. For instance, consider the execution of a conditional. Hence, if \(c' \leftrightarrow c''\) then any possible sequence of communication in \(c''\) is a sequence of communication in \(c'\). Considering the automata \(\tilde{A}\) and \(\tilde{A}'\) built during the analysis for \(c'\) and \(c''\), we have \(\tilde{A}' \prec \tilde{A}\). This is summed up by the proposition below.

**Proposition 2** (Sequential subject reduction) If \(\tilde{C}, \tilde{\mathcal{E}}, \tilde{A} \vdash c'\) and \(c' \leftrightarrow c''\) then \(\tilde{C}, \tilde{\mathcal{E}}, \tilde{A}' \vdash c''\) for some \(\tilde{A}'\) such that \(\tilde{A} \prec \tilde{A}'\).

Analyzing separately the processes of an application allows us to order the synchronization points on each process. However this is not enough to obtain a fine approximation of the topology of communications. In the following Section, we consider the product automaton of the automata described above in order to compute the synchronization realized by the program.

### 5 Process Pool Analysis

In this Section, we focus on analyzing a process pool. Section 5.1 defines an analysis \(\models^3\) based on the product automaton \(\lambda^3(\tilde{S})\) of a collection \(\tilde{S}\) of automata built as in Section 4. We prove the correctness of \(\models^3\) by a subject reduction property. In Section 5.2, we introduce a second analysis \(\models^4\) based on a reduced product automaton \(\lambda^4(\tilde{S})\) which size is polynomial in the size of the original program. We prove that \(\models^4\) is a correct approximation of \(\models^3\).

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\(^1\) A subset \(X\) of a complete lattice \((L, \leq)\) is a Moore family iff for all set \(Y \subseteq X\), \((\forall Y) \in X\). Notice that a Moore family \(X\) never is empty and admits a least element since \((\forall X) \in X\) (see [20]).
5.1 Product Automaton Based Analysis

Let $\hat{S} = (\hat{A}_p)_{p \in \text{Dom}(P)}$ be a collection of automata. The states in the product
automaton $\hat{\lambda}^n_0(\hat{S})$ are products of the states of the automata in $\hat{S}$. An $\varepsilon$-transition
inside an automaton $\hat{A}_p$ denotes a sequential reduction step and is transcribed
in $\lambda^0_0(\hat{S})$. An $l$-transition in $\hat{A}_p$ denotes a synchronization point. We add $(l,l')$-transitions in $\lambda^0_0(\hat{S})$ if $l$ and $l'$ are transitions in two automata of $\hat{S}$ such that
the instruction labeled $l$ may synchronize with the instruction labeled $l'$.

**Definition 3 (Product automaton)** Let $\hat{S}$ be a collection of $n$ automata. The product automaton $\lambda^0_0(\hat{S})$ of the automata in $\hat{S}$ is a tuple $(\Sigma^l, q^l_0, Q^l, \delta^l)$. The alphabet is $\Sigma^l \subseteq \text{Lab}^l \cup \{\varepsilon, \mu\}$. $Q^l$ is made of $k$-tuples $(s_1, \ldots, s_k)$ in which $s_i$
$1 \leq i \leq k$, denotes the advancement of the $i$th automaton. $q^l_0$ is the initial state
and $\delta^l : (Q^l \times \Sigma^l) \rightarrow q(Q^l)$ is the transition function. $\lambda^0_0(\hat{S})$ is built as follows.

(i) The initial state $q^l_0 \in Q^l$ is the product of the initial states $\text{Start}(\hat{A}_p)$ of the
automata $\hat{A}_p \in \hat{S}$, i.e.

$$q^l_0 \triangleq \prod_{\hat{A}_p \in \hat{S}} \text{Start}(\hat{A}_p)$$

(ii) For all $q^l \in Q^l$ such that $q^l = (s_0, \ldots, s_n)$,

(a) $\forall i, 1 \leq i \leq n, \forall \ell \in \{\varepsilon, \mu\}, \exists \hat{A}_p \in \hat{S} : [s_i \xrightarrow{\ell} s_i] \in \hat{A}_p \Rightarrow$

(b) $\forall i, j, 1 \leq i \neq j \leq n,$

$$\bigg(\exists \hat{A}_p, \hat{A}_p' \in \hat{S} : \begin{cases} [s_i \xrightarrow{\ell} s_i] \in \hat{A}_p \\ [s_j \xrightarrow{\ell} s_j] \in \hat{A}_p' \\ (\text{send } c^l_{ij})_{\ell_i} \in \text{pry} \\ (\text{receive } c^l_{ji})_{\ell_j} \in \text{pry} \bigg) \Rightarrow [q^l \xrightarrow{\ell_{ij}} (s_0, \ldots, s_i, \ldots, s_j, \ldots, s_n)] \in \lambda^0_0(\hat{S})$$

(c)

$$\forall i, 1 \leq i \leq n,$

$$\bigg(\exists \hat{A}_p \in \hat{S} : \begin{cases} [s_i \xrightarrow{\ell} s_i] \in \hat{A}_p \\ (\text{fork } c^l_{ii})_{\ell_i} \in \text{pry} \bigg) \Rightarrow [q^l \xrightarrow{\ell_i} (s_0, \ldots, s_i, \ldots, s_n)] \in \lambda^0_0(\hat{S})$$

The product automaton is built incrementally. We start with the only state $q^l_0$ and any state $q^l = (s_1, \ldots, s_k)$ in $\lambda^0_0(\hat{S})$ describes one of the possible advancements of the process pool. We add an $\varepsilon$-transition going out of $q^l$ every-time there is an $\varepsilon$-transition in one of the automata $\hat{A}_p$ going out of $s_i$, $1 \leq i \leq k$. Doing so, we obtain a new state which also denotes one of the possible advancements.

Next, an $l$-transition denotes a synchronization point. Two processes may communicate together if they own matching synchronization points $l_s$ and $l_r$. 

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related to an emission and a reception and which possibly are active at the same time. In this case there is a state \( q^2 = (s_1, \ldots, s_k) \) and two automata \( \hat{A}_p \) and \( \hat{A}_{p'} \) in \( \hat{S} \) such that \([s_s \xrightarrow{t_s} s'_s] \subseteq \hat{A}_p \) and \([s_r \xrightarrow{t_r} s'_r] \subseteq \hat{A}_{p'} \), \( 1 \leq s, r \leq k, s \neq r \).

Then the transition \( q^1 \xrightarrow{t_{p,p'}} q^2 \) is added to the product automaton where \( q^2 \) is a new state obtained by substituting \( s'_r \) and \( s'_s \) to \( s_s \) and \( s_r \) in \( q^2 \).

Notice that the product automaton allows a communication between any emitter \( s \) and any receptor \( r \) which possibly are active at the same time, independently of the channel they use. In Definition 4, the abstract value sent by \( s \) is added to the abstract value attached to \( r \) iff \( s \) and \( r \) possibly communicate on the same channel. Greater precision would be obtained by directly discarding these impossible communications in \( \lambda_{P}^{s}(\hat{S}) \). However, the product automaton would not be defined independently of the analysis, overloading the notations.

Finally, a fork creates a new process. Hence, for any transition \([s_{f} \xrightarrow{t_{f}} s_{f'}] \subseteq \hat{A}_p \) such that there exists a state \( q^2 = (s_1, \ldots, s_{f'}, \ldots, s_k) \) in \( \hat{S} \), the \((k+1)\) product state \((s_1, \ldots, s_{f'}, \ldots, s_k)\) is added to \( \hat{S} \). This tuple denotes the state resulting from the execution of the fork. It contains one more component \( s_{f'} \) which describes the advancement of the new process. Concerning the father process, the state denoting its advancement is updated to indicate that the process creation is done.

In the remainder of this Section, we specify the conditions a triple \((\hat{C}, \hat{E}, \hat{A}_0)\) has to satisfy to be an analysis for a process pool \( P \) and we prove the correctness of this analysis. \( \hat{C} \) and \( \hat{E} \) are defined in the same way as in Section 4 and \( \hat{A}_0 \) is the product of the automata \( \hat{A}_p \) built for the sequential expressions of \( P \). Also, we introduce a new order relation extending \( \leq \) to deal with the way the product automata are related under concurrent reduction.

\( \hat{C}, \hat{E}, \hat{A}_0 \models P \) denotes that the triple \((\hat{C}, \hat{E}, \hat{A}_0)\) is an analysis for the process pool \( P \), where \( \models \) is specified in Definition 4. Intuitively, we require that all the expressions in the process pool are correctly abstracted using the automaton \( \hat{A}_0 \) and that for any communication described in \( \hat{A}_0 \), the abstract value sent by the emitter is contained in the abstract value attached to the receptor.

**Definition 4** A triple \((\hat{C}, \hat{E}, \hat{A}_0)\) defines good annotations for a process pool \( P \), denoted \( \hat{C}, \hat{E}, \hat{A}_0 \models P \) iff

(i) \( \hat{A}_0 = \lambda_{P}^{s}(\hat{S}) \) for some collection \( \hat{S} \) of automata such that \( \hat{S} = (\hat{A}_p)_{p \in \text{Dom}(P)} \) and \( \hat{C}, \hat{E}, \hat{A}_p \models e \) for all \( \langle p : e \rangle \in P \).

(ii) For all \([q \xrightarrow{t_{q,q'}} q'] \subseteq \hat{A}_0 \) s.t. \( \{(\text{send } e_0)_{\langle p : e \rangle} \in pry, \hat{C}(l_0) \cap \hat{C}(l_2) \neq \emptyset\} \implies \hat{C}(t_1) \subseteq \hat{C}(t_2) \).

As stated in Section 4, the abstract cache \( \hat{C} \) indifferently is used to collect abstract values denoting either channels or functions. In Definition 4, \( e_0 \) and \( e_2 \) have type Channel and consequently the condition \( \hat{C}(l_0) \cap \hat{C}(l_2) \neq \emptyset \) considers abstract values related to channels in order to determine whether the emitter
and the receptor may communicate. If so, the constraint \( \hat{C}(l_1) \subseteq \hat{C}(l_r) \) involves abstract values denoting channels or functions, depending on the type of \( c_{f_{l}} \).

Similarly to Section 4, the existence of a least analysis for a process pool \( P \) stems from the fact that the set \( \{ (\hat{C}, \hat{E}, \hat{A}_\hat{A}) : \hat{C}, \hat{E}, \hat{A}_\hat{A} \models P \} \) of correct analysis for a process pool \( P \) is a Moore family w.r.t. the relation \( \prec \) of Equation (2).

In order to prove that an analysis is preserved under reduction, we introduce a new order relation \( \leq \) which extends \( \prec \) as follows. A path in \( \hat{A}_\hat{A} \) describes the sequence of synchronizations realized by one possible execution of the program. This relation allows us to state that if \( K, P \xrightarrow{[\ell]} K', P' \) then the product automaton \( \hat{A}_\hat{A}' \) which abstracts \( P' \) contains all the paths in \( \hat{A}_\hat{A} \) starting from \( q_0' \) where \( q_0' \) is a state we access from \( q_0' \) by an \( \ell \)-transition. So, any sequence of synchronizations in \( \hat{A}_\hat{A} \) is a sequence of synchronization in \( \hat{A}_\hat{A}' \) following the synchronization described by \( \ell \).

**Definition 5** Let \( \hat{A}_\hat{A} \) and \( \hat{A}_\hat{A}' \) be two product automata and \( \ell \in \Sigma \) a letter in the alphabet. \( \hat{A}_\hat{A} \leq \hat{A}_\hat{A}' \) iff for each path labeled \( \ell_1 \ldots \ell_n \) in \( \hat{A}_\hat{A} \), there exists a path labeled \( \ell, \ell_1 \ldots ,\ell_n \) in \( \hat{A}_\hat{A}' \).

Notice that \( \hat{A}_\hat{A} \leq \hat{A}_\hat{A}' \) iff \( \hat{A} \ll \hat{A}' \). Finally, we introduce the following property which describes how the annotations behave under reduction.

**Proposition 6 (Concurrent subject reduction)** Let \( P \) be a process pool such that \( \hat{C}, \hat{E}, \hat{A}_\hat{A} \models P \). Then if \( K, P \xrightarrow{[\ell]} K', P' \) then \( \hat{C}, \hat{E}, \hat{A}_\hat{A}' \models P' \) for some product automaton \( \hat{A}_\hat{A}' \) such that \( \hat{A}_\hat{A} \leq \hat{A}_\hat{A}' \).

### 5.2 Polynomial Size Analysis

In this Section, we introduce an automaton \( \lambda_\hat{A}(\hat{S}) \) which is a reduced version of \( \lambda_\hat{A}(\hat{S}) \). While the size of the product automaton is possibly exponential in the size \( n \) of the program, \( \lambda_\hat{A}(\hat{S}) \) has size \( O(n^2) \). We show that \( \lambda_\hat{A}(\hat{S}) \) can be used instead of \( \lambda_\hat{A}(\hat{S}) \) for the analysis.

Intuitively, the only relevant informations needed by the analysis are the pairs of synchronizations points which may interact together. For instance, we are interested in the set of potential emission points which may communicate with a given reception point, independently of any notion of ordering, i.e. of the place of this communication in a trace of the execution.

The product automaton \( \lambda_\hat{A}(\hat{S}) \) contains all the sequences of synchronizations of the whole possible executions, which is more precise than needed for our purpose. Hence, we reduce its size by discarding such irrelevant informations and still keeping enough precision to eliminate non possible synchronizations.

This compromise is obtained in \( \lambda_\hat{A}(\hat{S}) \) by building an automaton able to answer to the question: "which are the possible synchronization points possibly
following a given synchronization? Let $l_0$ and $l_1$ be the labels of matching synchronization points. The state $q_{l_0,l_1}$ denotes this synchronization in $A_0^1(\hat{S})$ and the set of synchronization points following the interaction is given by $L(q_{l_0,l_1}).$

A new synchronization between points $l_2$ and $l_3$ is allowed if both $l_2$ and $l_3$ belong to $L(q_{\ell'})$ for some previous synchronization denoted by $q_{\ell'}.$ In this case, we add an $(l_2,l_3)$-transition from $q_{\ell'}$ to $q_{l_0,l_1}$ and $L(q_{l_2,l_3})$ is updated.

Let $S$ denote the set of states terminating an $(l_s,l_r)$-transition in $A_0^1(\hat{S}).$ $S$ is approximated in $A_0^1(\hat{S})$ by a single state $q_{l_s,l_r}.$ Hence, any $(l_s,l_r)$-transition in $A_0^1(\hat{S})$ terminates in $q_{l_s,l_r}.$ In addition, $A_0^1(\hat{S})$ keeps track of the advancement of the sequential automata after the synchronization denoted by $(l_s,l_r)$ via $L(q_{l_s,l_r})$ which contains all the nodes $s$ such that $s$ occurs in $q$ for some $q \in S.$

**Definition 7 (Reduced product automaton)** Let $\hat{S}$ be a collection of $n$ automata. The reduced product automaton $A_0^1(\hat{S})$ of the automata in $\hat{S}$ is a tuple $(\Sigma^2, A_0^1(\hat{S}), Q^2, S^2, L).$ The alphabet is $\Sigma^2 = \text{Lab}^2.$ The states belong to $Q^2 = \{q_{l,l'} : l,l' \in \text{Lab} \} \cup \{q_0^2\}$ where $q_0^2$ is a fresh initial state. $S^2 \subset (Q^2 \times \Sigma^2) \rightarrow \varphi(Q^2)$ is the transition function. $L : Q^2 \rightarrow \varphi(Q_P),$ where $Q_P = \bigcup_{\hat{A}_p \in \hat{S}} \text{States}(\hat{A}_p),$ assigns to any state $q_{l,l'}$ the possibly active states of the $\hat{A}_p$ in $\hat{S}$ once the points $l$ and $l'$ have synchronized together. $A_0^1(\hat{S})$ is built as follows.

(i) $q_0^2 \in Q^2$ and $L(q_0^2) = \bigcup_{\hat{A}_p \in \hat{S}} \text{Start}(\hat{A}_p).$

(ii) For all $q \in Q^2,$

(a) $\forall s \in L(q), \ (\exists \hat{A}_p \in \hat{S} : [s \xrightarrow{r} s'] \subset \hat{A}_p) \Rightarrow \{s'\} \subset L(q).$

(b) For all $s \in L(q),$ $r \in L(q),$ $s \xrightarrow{r} s' \in \text{proj},$

\[
\begin{array}{c}
\exists \hat{A}_p, \hat{A}_p' \in \hat{S} : \\
\left[ \begin{array}{c}
\left[ s \xrightarrow{g} s' \right] \subset \hat{A}_p \\
\left[ s \xrightarrow{g} s' \right] \subset \hat{A}_p' \\
\text{(send $c_0^0$ $c_1^2$)} \in \text{proj} \\
\text{(receive $c_2^0$)} \in \text{proj}
\end{array} \right. \\
\Rightarrow \\
\left[ \begin{array}{c}
\left[ l \xrightarrow{c_0^0} q_{l_0,l_1} \right] \subset A_0^1(\hat{S}) \\
\left[ l \xrightarrow{c_2^0} q_{l_2,l_3} \right] \subset A_0^1(\hat{S}) \\
\left[ r \xrightarrow{c_1^2} q_{l_4,l_5} \right] \subset A_0^1(\hat{S}) \\
\{s_{l_0}, s_{l_1}\} \subset L(q_{l_0,l_1}) \\
\{s_{l_2}, s_{l_3}\} \subset L(q_{l_2,l_3}) \\
\end{array} \right. \\
\end{array}
\]

(iii) $\forall s \in L(q),$ $s \xrightarrow{r} s' \in \text{proj},$

\[
\begin{array}{c}
\exists \hat{A}_p \in \hat{S} : \\
\left[ \begin{array}{c}
\left[ l \xrightarrow{c_1^0} q_{l_0,l_1} \right] \subset \hat{A}_p \\
\left[ l \xrightarrow{c_1^0} q_{l_0,l_1} \right] \subset \hat{A}_p' \\
\text{(fork $c_0^0$)} \in \text{proj}
\end{array} \right. \\
\Rightarrow \\
\left[ \begin{array}{c}
\left[ l \xrightarrow{c_1^0} q_{l_0,l_1} \right] \subset A_0^1(\hat{S}) \\
\left[ l \xrightarrow{c_1^0} q_{l_0,l_1} \right] \subset A_0^1(\hat{S}) \\
\{s_{l_0}, s_{l_1}\} \subset L(q_{l_0,l_1}) \\
\{s_{l_2}, s_{l_3}\} \subset L(q_{l_2,l_3}) \\
\end{array} \right. \\
\end{array}
\]

$A_0^1(\hat{S})$ is built incrementally. We start with a fresh initial state $q_0^2.$ $L(q_0^2)$ contains the initial states of the automata in $\hat{S}$ indicating that the related points belong to the set of possible active points at the beginning of the execution. An $\varepsilon$-transition in some $\hat{A}_p \in \hat{S}$ denotes an internal reduction step of the process $p.$ Hence, every time that $s \in L(q)$ and $[s \xrightarrow{r} s'] \subset \hat{A}_p$ for some $s \in Q_P$ and $q \in Q^2,$ $s'$ is added to $L(q)$ indicating that $s'$ also is a possibly active point after execution of the synchronization denoted by $q$ and before any other synchronization. This is done in (2a).
Next, consider the transitions $[E_1 \xrightarrow{l_1} s_{l_1}]$ and $[E_2 \xrightarrow{l_2} s_{l_2}]$ of two automata $\tilde{A}_p$ and $\tilde{A}_{p'}$. The communication is possible if $E_1$ and $E_2$ possibly are active at the same time, i.e., if $E_1$ and $E_2$ both belong to $L(q)$ for some state $q \in Q^p$. If so, the transition $[q \xrightarrow{L} q_{s_{l_1}l_2}]$ is added to $\alpha_0^p(\tilde{S})$ and $L(q_{s_{l_1}l_2})$ has to contain the active points once the synchronization has been done. Hence $L(q)$ contains the points active before the synchronization except $E_1$ and $E_2$, as well as the points following the communication, namely $s_{l_1}$ and $s_{l_2}$. This is done in (6), for both states are treated similarly in (7). A new state $q_{s_{l_1}l_2}$ is added to $Q^p$ and $L(q_{s_{l_1}l_2})$ is updated in the same way as for communications.

**Fig. 7.** a) Collection of automata for the program of Fig. 1. b) Reduced product automaton for the previous collection. Values in front of the states correspond to $L(q)$.

Figure 7 shows the reduced product automaton corresponding to the example of Figure 1. In this example, one can see that there is an exact correspondence between the states of the product automaton and the values of the function $L$. In this case, the reduced automaton is as precise as the product automaton and strictly describes the same possible communications.

Based on $\alpha_0^p(\tilde{S})$, we introduce a new analysis $\models^{\hat{\cdot}}$ for a process pool $P$. Again, we used the sets $\hat{\cdot}$ and $\hat{\cdot}^p$ defined in Section 4 as well as a reduced product automaton denoted $\tilde{A}_p^\hat{\cdot}$. $\models^{\hat{\cdot}}$ is obtained by substituting the reduced product automaton to the product automaton in the definition of $\models^{\hat{\cdot}}$.

**Definition 8** A triple $(\hat{\cdot}, \hat{\cdot}^p, \tilde{A}_p^\hat{\cdot})$ defines good reduced annotations for a process pool $P$, denoted $\hat{\cdot}, \hat{\cdot}^p, \tilde{A}_p^\hat{\cdot} \models^{\hat{\cdot}} P$ iff

\[(i) \tilde{A}_p^\hat{\cdot} = \alpha_0^p(\tilde{S}) \text{ for some collection } \tilde{S} \text{ of automata such that } \tilde{S} = (\tilde{A}_p)_{p \in \text{Dom}(P)} \text{ and } \hat{\cdot}, \hat{\cdot}^p, \tilde{A}_p^\hat{\cdot} \models^{\hat{\cdot}} e^p \text{ for all } \langle p : e^p \rangle \in P.\]
(ii) For all $\tilde{q} \in \lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ s.t. $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ implies $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$.

Again, the existence of a least analysis in the sense of $\prec$ stems from the fact that $\{(\tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_0) : \tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_0 \models^\prec P\}$ is a Moore family.

The reduced automaton $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ collapses the states of the product automaton $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ following a given synchronization. As a consequence, any transition going out of a state $q^2$ in $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ corresponds to an outgoing transition going out of $q^2$ in $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$. Hence any path in $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ also is a path in $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$. We use this observation to prove $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ may be substituted to $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$. Doing so we state that the reduced automaton contains all the sequences of synchronizations of the product automaton.

**Proposition 9 (Equivalence of automata)** Let $P$ be a process pool and $\tilde{S} = (\tilde{\mathcal{A}}_p)_{p \in \text{Dom}(P)}$ a collection of automata such that for all $p : e^1 \in P$, $\tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_p \models^\prec e^1$. We have $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S}) \leq \lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$.

Hence, $\models^\prec$ can be used instead of $\models^\prec$ without discarding the properties established in Section 5.1. So, if $\tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_0 \models^\prec P$ then $\tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_0 \models^\prec P$ for some $\tilde{\mathcal{A}}_0$ such that $\tilde{\mathcal{A}}_0 \leq \tilde{\mathcal{A}}_0$. We end this Section by introducing the following property about the size of the reduced product automaton.

**Proposition 10 (Size of the reduced automaton)** Let $\tilde{S}$ be a collection of $k$ automata of size $O(n)$ and let $n = km$. The reduced product automaton $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ has size $O(n^4)$.

Proposition 10 stems from the following observations. There is at most $O(n)$ synchronization points in the automata. So, the number of states in $\lambda_{\tilde{\mathcal{A}}}^k(\tilde{S})$ is $O(n^2)$ and the transition function has size $O(n^4)$. For a given $q \in Q_k$, $L(q)$ contains all the labels of the program in the worst case, i.e. $L(q)$ has size $O(n)$. Hence $L$ has size $O(n^3)$ and the whole size of the automaton consequently is $O(n^4)$, due to the transition function.

Because of the size limitations, we do not show how to automatically compute an analysis for a process pool $P$. However, one can generate a set $c[P]$ of constraints such that a solution $(\tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_0)$ to $c[P]$ satisfies $\tilde{C}, \tilde{\mathcal{E}}, \tilde{\mathcal{A}}_0 \models^\prec P$. The least solution can be computed in polynomial time. One method consists of using a graph formulation of the constraints $[20, 26]$. Each set constrained by $c[P]$ corresponds to a node in a graph $G$ and edges are related to constraints. In this case, the complexity of the resolution of $c[P]$ for a process pool $P$ described by a program of size $O(n)$ stems from the following observations. $G$ contains at most $O(n^4)$ vertices and $O(n^4)$ edges. A node is examined as many times as its related value is modified. Since values have size $O(n^2)$, the whole complexity of this resolution method is $O(n^6)$.
6 Application

In this Section, we comment the results given by an implementation of the CFA. We consider a virtual circuit allocation mechanism similar this of ATM [1, 27].

6.1 Virtual Circuit Allocation

Virtual circuit creation follows the scheme of Figure 8. The different nodes of the network are linked to their neighbors by control channels. These channels are used to forward a circuit creation message from the source node to the destination node. The latter creates a link with its neighbor which proceeds similarly until the source node is reached (still using control channels). Next, each intermediary node propagates the data received on its input link to its output link.

![Diagram of virtual circuit allocation](image)

Fig. 8. Principle of virtual circuit allocation.

A full implementation of this mechanism was realized in the core language treated in this article. For simplicity, we give a simplified version written in an imperative pseudo-language (Figure 9). We only use one intermediary node.

We assume that processes \( p_1 \), \( p_2 \) and \( p_3 \) are concurrently running on three different nodes of the network. \#ctrl\_12 (resp. \#ctrl\_23) is a bidirectional control channel linking the nodes of \( p_1 \) and \( p_2 \) (resp. \( p_2 \) and \( p_3 \)). In addition, since \( p_1 \) creates two different circuits \( a \) and \( b \), the pieces of code given for process \( p_2 \) are executed twice. In our implementation, we use a 0-CFA [20] for the sequential parts of the language. This enforces us to duplicate these pieces of code in order to keep enough precision. It could be avoided by using a more sophisticated analysis (1-CFA). So, in Figure 9, the two labels annotating some instructions correspond to the unique labels of two copies of the same piece of code.

\( p_1 \), \( p_2 \) and \( p_3 \) work as follows. \( p_1 \) successively asks for two new circuit creations. Function `connect` sends an initialization message on the control channel \#ctrl\_12 and binds variable `virt\_cha` to the channel name to be used for \( a \).
This scheme is repeated for \( b \). Finally, data are transmitted on \texttt{virt.ch.a} and \texttt{virt.ch.b}.

\( p_2 \) corresponds to the node between the source and the destination. When a virtual circuit creation request is received on \texttt{#ctrl.12}, it is transmitted to the destination on the control channel \texttt{#ctrl.23}. \( p_2 \) next receives the output channel name to use and binds variable \texttt{ch.out}. Next, it creates a new channel \texttt{ch.in} which is sent to the source process. The last operation consists of creating a new process which forwards on \texttt{ch.out} the values received on \texttt{ch.in}. The whole process is repeated for the virtual circuit \( b \).

Finally, \( p_3 \) corresponds to the destination node. When the control message is received, \( p_3 \) creates a new channel and binds variable \texttt{ch.in}. This channel name is transmitted to \( p_2 \) via control channel \texttt{#ctrl.23}. Once the circuits are created, \( p_3 \) receives data on \texttt{a} and \texttt{b}.

### 6.2 Analysis

We used our analysis to check the correctness of a full implementation of the program described in Figure 9. Here we present the results we obtained, translated to the program of Figure 9. Figure 10 gives the abstract values obtained for the points of interest and for relevant variables. As indicated above, some pieces of code are executed twice. In Figure 10, we indicate for variables which execution is referenced by \((a)\) and \((b)\).

The main observation concerns the abstract values received by \( p_3 \) at points 132 and 134. These values, \( \{30\} \) \texttt{and} \( \{35\} \), are the ones emitted by \( p_1 \) on circuits \texttt{a} and \texttt{b}. This validates the mechanism. The values received on \texttt{a} \texttt{(resp.} \texttt{b}) are the ones emitted on this channel and only these ones. This guarantees that \( p_2 \) does not invert the data received on its input links and forwarded on its output links.

Concerning the virtual circuit \( a \), one can check that the variables \texttt{virt.ch.a} and \texttt{ch.dest} \((a)\) corresponding to the extremities of the circuit are bound to \( \{58\} \) \texttt{and} \( \{124\} \) which correspond to the channel names created at these points. The new process created by \( p_2 \) forwards on a name created at point 124 the data.
received on a name created at point 58 (variables chain (a) and chout (a)).
Thus, the transmission of data from the source to the destination is ensured.
In addition, it is possible to check that the channel names of abstract value
{58} and {124} are not used elsewhere in the program. So, the data emitted
on the virtual circuit a only are transmitted to the destination node. Similar
observations can be done for the virtual circuit b.

Finally, let us take note that in order to obtain these results, the analysis has
to use an approximation of the communication topology of the program. The
same control channels #ctrl12 and #ctrl23 are used for both circuit creation.
An analysis based on Equation (1) could not obtain these results. The result
would be virt.cha = virt.cha = {58,91} which does not enable us to check
that no confusion is done between the data transmitted on both circuits.

We also used this analysis in order to verify security properties for an auction
distributed application. This system is made of a server and several clients who
send to the server public data (e.g., their price) as well as confidential data (e.g.,
their credit card number). Each client is linked to the server by one communi-
cation channel. The server broadcasts the public data and conserves the private
ones. We show that, in our implementation, the only public data are actually
broadcasted.

7 Conclusion

In this article, we introduced a static analysis able to depict a conservative image
of the communication topology of a concurrent system written in Concurrent
ML. The dynamic aspects of concurrency are managed, including forks, the
transmission of functions and channel creations. This analysis is a CFA which
builds a finite automaton in order to increase its precision. It was implemented
and results are discussed in Sections 2 and 6.

Using product automata allows us to derive a fine approximation of the topol-
ogy, as shown in Section 2. This represents the main contribution of this article.
It enables to minimize the abstract value attached to reception points. Gains
are twofold. First, this increase the precision of the analysis for the sequential
part of the program following the reception. Second, since channel names are
potentially communicated, we also define more concisely the pairs of possible emitters and receptors for further communications.

We believe that the gains due to the introduction of topological information in the CFA improve the analysis of many kinds of applications. For quite common deterministic communication schemes, such as a sequence of communications on the same channel between two processes, this makes possible to distinguish between the values sent at each stage. For more complicated communication schemes, precision may decrease at some points in the program, for instance because of alternatives, loops or non-determinism. However, precision will increase again once the ambiguities disappear, for instance after a global synchronization.

Now, we are interested in mixing it with other analyses, in order to increase the conciseness of the annotations. We focus on a binding time analysis [8, 12] which uses topological informations during its analysis of programs. In this context, our CFA would enable to distinguish between the static and dynamic values sent on the same channel, while usual analyses consider a channel dynamic as soon as a dynamic value is sent on it.

References